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3. **Piece-wise linear approximation of MOS nonlinear junction capacitance in high-frequency ci**
   design
   Ma, S.W.; Hei Wong; Ho, C.K.;
   Electron Devices and Solid-State Circuits, 2003 IEEE Conference on
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AbstractPlus | References | Full Text: PDF(324 KB)   IEEE JNL

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AbstractPlus | References | Full Text: PDF(358 KB)   IEEE JNL

12. **A simple yet comprehensive unified physical model of the 2D electron gas in delta-doped ar high electron mobility transistors**
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Electron Devices, IEEE Transactions on
Volume 47, Issue 1, Jan. 2000 Page(s):11 - 23
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AbstractPlus | References | Full Text: PDF(384 KB)   IEEE JNL

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AbstractPlus | Full Text: PDF(440 KB)   IEEE JNL

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15. Analytical transient response and propagation delay evaluation of the CMOS inverter for she
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AbstractPlus | References | Full Text: PDF(228 KB)   IEEE JNL

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18. Parametric yield estimation for a MOSFET integrated circuit
Berrah, C.M.;
Circuits and Systems, 1990., IEEE International Symposium on
1-3 May 1990 Page(s):2260 - 2263 vol.3
Digital Object Identifier 10.1109/ISCAS.1990.112458

AbstractPlus | Full Text: PDF(372 KB)   IEEE CNF

19. Analysis of communication circuits based on multidimensional Fourier transformation
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AbstractPlus | References | Full Text: PDF(396 KB)   IEEE JNL

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1. **High-frequency AC hot-carrier degradation in CMOS circuits**
   Vei-Han Chan; Kopley, T.E.; Marcoux, P.; Chung, J.E.;
   Electron Devices Meeting, 1994. Technical Digest., International
   11-14 Dec. 1994 Page(s):299 - 302
   Digital Object Identifier 10.1109/IEDM.1994.383408

   AbstractPlus | Full Text: PDF(260 KB)   IEEE CNF

2. **Extraction of the main current components of floating-body partially-depleted SOI devices**
   Fenouillet-Beranger, C.; Raynaud, C.; Faynot, O.; Grouillet, A.; Du Port de Pontcharra, J.; Tabone,
   SOI Conference, 2000 IEEE International
   2-5 Oct. 2000 Page(s):70 - 71
   Digital Object Identifier 10.1109/SOI.2000.892774

   AbstractPlus | Full Text: PDF(116 KB)   IEEE CNF

3. **Modeling and stability analysis of a DC power system with solid state power controllers**
   Panov, Y.V.; Lee, F.C.;
   Applied Power Electronics Conference and Exposition, 1996. APEC '96. Conference Proceedings ·
   Annual
   Volume 2, 3-7 March 1996 Page(s):685 - 691 vol.2
   Digital Object Identifier 10.1109/APEC.1996.500514

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                              Circuits and Systems, IEEE Transactions on
                              Volume 38, Issue 12, Dec. 1991 Page(s):1439 - 1450
                              Digital Object Identifier 10.1109/31.108498

                              AbstractPlus | Full Text: PDF(880 KB)   IEEE JNL

                    ☐   2. Layout design considerations in MOS continuous-time integrated filters
                              Smith, S.; Ismail, M.; Chung-Chih Hung; Shu-Chuan Huang;
                              Circuits and Systems, 1994. APCCAS '94., 1994 IEEE Asia-Pacific Conference on
                              5-8 Dec. 1994 Page(s):300 - 305
                              Digital Object Identifier 10.1109/APCCAS.1994.514566

                              AbstractPlus | Full Text: PDF(488 KB)   IEEE CNF

                    ☐   3. Finite GB and MOS parasitic capacitance effects in a class of MOSFET-C filters
                              Ismail, M.; Kim, D.Y.; Shin, H.K.;
                              Circuits and Systems, 1990., Proceedings of the 33rd Midwest Symposium on
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